



Sneaker Tour April 8, 2005

- 7:45 a.m.** Registration and breakfast at ASU MacroTechnology Works (7700 S. River Pkwy)
- 8:15-8:25 a.m.** Park overview presented by Michele Pino
- 8:25-8:30 a.m.** ASU Technopolis presentation by Terree Wasley
- 8:30-9:00 a.m.** Flexible Display Center presentation by Greg Raupp ¹
- 9:00-9:45 a.m.** Tour of Flexible Display Center
- 9:45-10:00 a.m.** Attendees walk over to Lakeside Technology Center (7755 S. Research Dr.)
- 10:00-10:30 a.m.** **Tour 1:** CMC Interconnect Technologies ², or PADT ³
- 10:40-11:10 a.m.** **Tour 2:** CMC Interconnect Technologies or PADT
- 11:10-11:25 a.m.** Walk over to ASU MacroTechnology Works building
- 11:25a.m.-12:00p.m.** Networking at ASU MacroTechnology Works with all companies. Refreshments served.

¹ The U.S. Army awarded ASU a \$43.7 million, five-year cooperative agreement to establish the **Flexible Display Center** at the Park. This center will develop thin, flexible, low-power computer displays that can be rolled up or folded and put in a soldier's pocket.

² **CMC Interconnect Technologies** provides outsourced technical services to the electronics industry. CMC specializes in analytical testing, failure analysis, reliability testing, thermal/electrical characterization and technical consulting. CMC's testing capabilities include: SEM/EDS, Thermal/Electrical Measurements, Mechanical Testing, and Thermal/Environmental Stress Evaluations. CMC's material experience includes organics, plastics, ceramics, metals and related plating and interconnection.

³ **Phoenix Analysis & Design Technologies (PADT)** is a nearly 50 person company with a diversity of engineering resources and skill sets unmatched even by larger firms. PADT retains the technical disciplines required to design, fabricate, test, and deliver complete electromechanical systems to its customers in the aerospace, medical, semiconductor, thin film technology, energy, and consumer product fields.